



Logic Analyzer

OLA3200

- 32 data sampling channels, 2 external clock channels.
- Start, trigger, and delay functions for effective data tracking and recording.
- Large scale integrated circuits, FPGA, SOPC; high speed, large memory.
- Fast and effective debugging tool for hardware and software of MCU.
- 5.7-inch color LCD display.
- USB device and RS-232 interface.

Model		OLA3200
Input	Input Channel	32 data sampling channels, 2 external clock channels
	Threshold Voltage	6 independent and adjustable threshold voltages Adjustable Range: -6 V ~ +6 V Resolution: 0.1 V
	Input Impedance	Resistance >100 k, Capacitor <8 pf
Sample/Memory	Sampling Rate	1 Hz ~ 100 MHz (Period 10 ns ~ 1 s), Resolution: 10 ns
	Timing Rate	
	State Rate	1 Hz ~ 35 MHz
	Sampling Phase	Rising edge, falling edge
	Memory Depth	256 kbytes / channel
Trigger	Trigger Condition	32 bit start-select, 32bits start-compare word
	Event Count	1 ~ 999 times
	Memory Delay	1 ~ 260000 sampling cycles
Pattern Generator	Pattern Type	CH00 to CH15 are counters with adding 1
		CH16 to CH29 are shift pulse
		CH30 to CH31 monitor external clk1 and clk2
Pattern Rate	Frequency: 1 Hz ~ 50 MHz (period 20 ns ~ 1 s) resolution: 10 ns	
General Characteristics	Power	AC220 V (1 ± 10%), 50 Hz (1 ± 5%), ≤10 VA
	Display	5.7-inch TFT LCD
	Dimension & Weight	329 mm × 283 mm × 155 mm; approx. 4.3 kg

Accessories:

OLA3200-A1	3-Core Power Cable
OLA3200-A2	Test Hook (x40)
OLA3200-A3	Test Hook Connecting Cable (x40)
OLA3200-A4	50 Wire Cables and Connectors (x2)
OLA3200-A5	Input Transferring Boxes (x2)
OLA3200-A6	CD (Software + User Guide)